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January 31, 2005

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SHREEN K. DANAMRAJ DANAMRAJ & YOUST, P.C. PREMIER PLACE, STE. 1450 **DALLAS, TX 75206**

EXAMINER

BAKER, PAUL A

ART UNIT

PAPER NUMBER

6631

2188

1263-0009US

DATE MAILED: 10/29/2004

APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO 09/886,577 06/21/2001 Richard S. Rov

TITLE OF INVENTION: SIMULTANEOUS ACCESS AND CACHE LOADING IN A HIERARCHICALLY ORGANIZED MEMORY CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1370	\$0	\$1370	01/31/2005

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

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